

M/A-COM Products

Released, 29 Jun 07

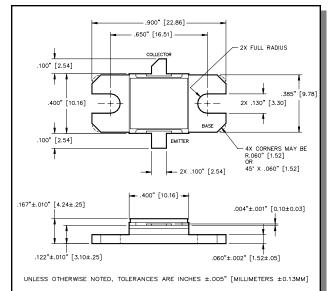
Radar Pulsed Power Transistor 5W, 2.9-3.1 GHz, 100µs Pulse, 10% Duty

Features

- NPN silicon microwave power transistors
- Common base configuration
- Broadband Class C operation
- High efficiency inter-digitized geometry
- · Diffused emitter ballasting resistors
- Gold metallization system
- · Internal input and output impedance matching
- Hermetic metal/ceramic package
- RoHS compliant

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Outline Drawing



Absolute Maximum Ratings at 25°C

Parameter	Symbol	Rating	Units
Collector-Emitter Voltage	V _{CES}	65	V
Emitter-Base Voltage	V _{EBO}	3.0	V
Collector Current (Peak)	Ι _C	0.7	А
Power Dissipation @ +25°C	P _{TOT}	50	W
Storage Temperature	T _{STG}	-65 to +200	°C
Junction Temperature	TJ	200	°C

Electrical Specifications: T_c = 25 ± 5°C (Room Ambient)

Parameter	Test Conditions	Frequency	Symbol	Min	Max	Units
Collector-Emitter Breakdown Voltage	I _C = 10mA		BV _{CES}	65	-	V
Collector-Emitter Leakage Current	V _{CE} = 40V		I _{CES}	-	1.5	mA
Thermal Resistance	Vcc = 36V, Pin = 1.0W	F = 2.9, 3.0, 3.1 GHz	R _{TH(JC)}	-	3.5	°C/W
Output Power	Vcc = 36V, Pin = 1.0W	F = 2.9, 3.0, 3.1 GHz	P _{OUT}	5.0	-	W
Power Gain	Vcc = 36V, Pin = 1.0W	F = 2.9, 3.0, 3.1 GHz	G _P	7.0	-	dB
Collector Efficiency	Vcc = 36V, Pin = 1.0W	F = 2.9, 3.0, 3.1 GHz	η _c	30	-	%
Input Return Loss	Vcc = 36V, Pin = 1.0W	F = 2.9, 3.0, 3.1 GHz	RL	-	-6	dB
Load Mismatch Tolerance	Vcc = 36V, Pin = 1.0W	F = 2.9, 3.0, 3.1 GHz	VSWR-T	-	3:1	-
Load Mismatch Stability	Vcc = 36V, Pin = 1.0W	F = 2.9, 3.0, 3.1 GHz	VSWR-S	-	1.5:1	-

ADVANCED: Data Sheets contain information regarding a product M/A-COM is considering for development. Performance is based on target specifications, simulated results, and/or prototype measurements. Commitment to develop is not guaranteed.

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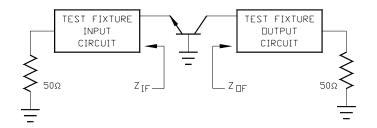
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Typical RF Performance

Freq. (GHz)	Pin (W)	Pout (W)	Gain (dB)	lc (A)	Eff (%)	RL (dB)	VSWR-S (1.5:1)	VSWR-T (3:1)
2.9	1.0	6.1	7.83	0.50	33.5	-16.7	S	Р
3.1	1.0	6.2	7.89	0.51	33.8	-20.5	S	Р

RF Test Fixture Impedance

F (GHz)	Z _{IF} (Ω)	Z _{OF} (Ω)
2.7	40 - j12	25 + j3.5
2.9	35 - j16	16 + j2.4
3.1	30 - j18	12 + j4.0



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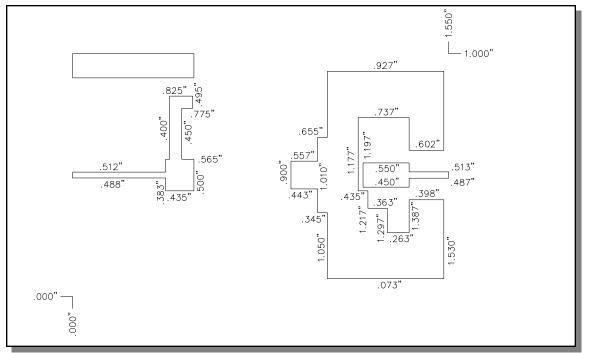
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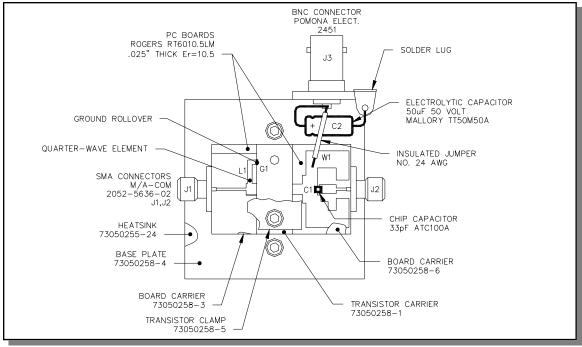
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Test Fixture Circuit Dimensions



Test Fixture Assembly



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